General Description

The SY89207L evaluation board is designed for convenient setup and quick evaluation of the SY89207L. The boards are optimized to interface directly to a 50Ω oscilloscope.

The default evaluation board I/O configuration is AC-coupled. For applications that require a DC-coupled configuration, step-by-step instructions for modifying the board are included.

The board is fully assembled and tested and is accompanied with all necessary documentation.

All data sheets and support documentation can be found on Micrel’s web site at www.micrel.com.

Features

- SY89207L
- +3.3V power supply
- AC-coupled configuration for ease-of-use
- Can be reconfigured for DC-coupled operation

Related Documentation

- SY89207L, 3.3V 800MHz Precision PECL Amplifier with Low-Gain Feedback Data Sheet

Evaluation Board
**Evaluation Board Description**

The SY89207L evaluation board is designed to operate at 3.3V without modification.

The default configuration is AC-coupled configuration. The evaluation board can be reconfigured in DC-coupled configuration; therefore the choice between two configurations offers the user flexibility for specific application.

**AC-Coupled Evaluation Board**

The AC-coupled configuration is suited for most customer applications and is preferred by the majority of users because of its ease-of-use. It requires only a single power supply and offers the most flexibility in interfacing to a variety of signal sources.

The DC-bias levels and AC-coupling capacitors are supplied on-board for each input, making it unnecessary to vary the offset voltage or change any components on the board over the +3.3V ±10% operating range. The user needs only to supply a minimum input voltage swing and the bias voltage will automatically adjust the input to the correct level as the power supply voltage varies.
**Evaluation Board**

SY89207L PECL Evaluation Board

<table>
<thead>
<tr>
<th>I/O</th>
<th>C7-C8</th>
<th>C5-C6, C9-C10</th>
<th>R1-R2, R5-R6</th>
<th>R3-R4</th>
<th>V&lt;sub&gt;DD&lt;/sub&gt;</th>
<th>GND</th>
<th>V&lt;sub&gt;EE&lt;/sub&gt;</th>
</tr>
</thead>
<tbody>
<tr>
<td>AC-Coupled Input/AC-Coupled Output&lt;sup&gt;(1)&lt;/sup&gt;</td>
<td>0.1µF</td>
<td>0.1µF</td>
<td>100Ω</td>
<td>50Ω</td>
<td>+3.3V</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>AC-Coupled Input/DC-Coupled Output&lt;sup&gt;(2)&lt;/sup&gt;</td>
<td>0.1µF</td>
<td>0Ω</td>
<td>NC</td>
<td>50Ω</td>
<td>+2V</td>
<td>0</td>
<td>-1.3V</td>
</tr>
<tr>
<td>DC-Coupled Input/DC-Coupled Output&lt;sup&gt;(3)&lt;/sup&gt;</td>
<td>0Ω</td>
<td>0Ω</td>
<td>NC</td>
<td>NC</td>
<td>+2V</td>
<td>0</td>
<td>-1.3V</td>
</tr>
</tbody>
</table>

**Table 1. SY89207L Configuration**

**Note:**
1. The default setting.
2. Replace C5-C6 and C9-C10 capacitors with 0Ω 0402 resistors, and remove R1-R2 and R5-R6 resistors.
3. Replace C5-C10 with 0Ω 0402 resistors and remove R1-R2, R3-R4, and R5-R6 resistors.
AC-Coupled Evaluation Board Setup

The following steps describe the procedure for setting up the evaluation board:

1. Set the voltage setting for a DC supply to 3.3V and turn off the supply.
2. Connect the GND terminal to the negative side of a DC power supply. This is the 0V ground potential.
3. Connect the VCC terminal to the positive side of a DC power supply.
4. Using a differential signal source set the amplitude of each side of the differential pair to 800mV (1600mV measured differentially). Set the offset to a positive value. The value of this offset is not critical, since the AC-coupled inputs will be automatically biased to the correct offset. Turn off or disable the outputs of the signal source.
5. Using equal length 50Ω impedance coaxial cables connect the signal source to the inputs on the evaluation board.
6. Using equal length 50Ω impedance coaxial cables, connect the outputs of the evaluation board to the oscilloscope or other measurement device that has an internal 50Ω termination.
7. Enable the signal source and supply voltage and monitor the outputs.
# Bill of Materials

<table>
<thead>
<tr>
<th>Item</th>
<th>Part Number</th>
<th>Manufacturer</th>
<th>Description</th>
<th>Qty.</th>
</tr>
</thead>
<tbody>
<tr>
<td>C1, C3, C5-C10</td>
<td>VJ0402Y104KXXAT</td>
<td>Vishay(1)</td>
<td>0.1µF, 25V, 10% Ceramic Capacitor, Size 0402, X7R Dielectric</td>
<td>8</td>
</tr>
<tr>
<td>C2, C4</td>
<td>293D685X0025C2T</td>
<td>Vishay(1)</td>
<td>6.8µF, 20V, Tantalum Electrolytic Capacitor, Size C</td>
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<tr>
<td>R3-R4</td>
<td>CRCW0402500F</td>
<td>Vishay(1)</td>
<td>50Ω, 1/16W, 5% Thick-film Resistor, Size 0402</td>
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</tr>
<tr>
<td>R1-R2, R5-R6</td>
<td>CRCW04021000F</td>
<td>Vishay(1)</td>
<td>100Ω, 1/16W, 5% Thick-film Resistor, Size 0402</td>
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<tr>
<td>GND</td>
<td>111-0703-001</td>
<td>Johnson Components(2)</td>
<td>Black Banana Jack</td>
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<td>VDD</td>
<td>111-0702-001</td>
<td>Johnson Components(2)</td>
<td>Red Banana Jack</td>
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<tr>
<td>VEE</td>
<td>111-0701-001</td>
<td>Johnson Components(2)</td>
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<tr>
<td>SMA1-SMA7</td>
<td>142-0701-851</td>
<td>Johnson Components(2)</td>
<td>Jack Assembly End Launch SMA</td>
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<tr>
<td>U1</td>
<td>SY89207L</td>
<td>Micrel(3)</td>
<td>PECL Amplifier with Low Gain Feedback</td>
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</table>

**Additional Components for AC-Coupled Outputs**

<table>
<thead>
<tr>
<th>Item</th>
<th>Part Number</th>
<th>Manufacturer</th>
<th>Description</th>
<th>Qty.</th>
</tr>
</thead>
<tbody>
<tr>
<td>C3-C8</td>
<td>VJ0402Y104KXXAT</td>
<td>Vishay(1)</td>
<td>0.1µF, 25V, 10% Ceramic Capacitor, Size 0402, X7R, Dielectric</td>
<td>6</td>
</tr>
</tbody>
</table>

**Notes:**
1. Vishay: [www.vishay.com](http://www.vishay.com)
2. Johnson Components: [www.johnsoncomponents.com](http://www.johnsoncomponents.com)
Evaluation Board Layout

PC Board Layout
The evaluation boards are constructed with Rogers 4003 material and are coplanar in design and fabricated to minimize noise, achieve high bandwidth and minimize crosstalk.

<table>
<thead>
<tr>
<th>Layer</th>
<th>SY89207L</th>
</tr>
</thead>
<tbody>
<tr>
<td>L1</td>
<td>GND and Signal</td>
</tr>
<tr>
<td>L2</td>
<td>GND</td>
</tr>
<tr>
<td>L3</td>
<td>VCC and VEE</td>
</tr>
<tr>
<td>L4</td>
<td>GND</td>
</tr>
</tbody>
</table>

Table 2. Layer Stack

DC-Coupled Evaluation Board

AC-Coupled Inputs/DC-Coupled Outputs
1. Replace C5-C6 and C9-C10 with 0Ω 0402 resistors.
2. Remove resistors R1-R2 and R5-R6.

DC-Coupled Inputs/DC-Coupled Outputs
1. Replace C5-C10 with 0Ω 0402 resistors.
2. Remove R1-R2, R3-R4, and R5-R6 resistors.
Micrel Cross Reference
To find an equivalent Micrel part, go to Micrel’s website at: http://www.micrel.com and following the steps below:
1. Click on Dynamic Cross Reference
2. Enter competitor’s part number in the Dynamic Cross Reference field
3. To download a PDF version of this information, click on the Cross Reference PDF tab

Application Hints and Notes
For application notes on high speed termination on PECL and LVPECL products, clock synthesizer products, SONET jitter measurement, and other High Bandwidth product go to Micrel’s website at http://www.micrel.com/. Once in Micrel’s website, follow the steps below:
1. Click on “Product Info”.
2. In the Applications Information Box, choose “Application Hints and Application Notes.”

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